



SREE VAHINI INSTITUTE OF SCIENCE & TECHNOLOGY, TIRUVURU

ECE DEPARTMENT FACULTY

A) Profile of each faculty give one page covering with passport size photograph

1. Name of the faculty: **Mr.K. Venkanna**
2. Date of Birth: 05-03-1982
3. Unique ID: 872033224066
4. Education qualifications: M. Tech (VLSI System Design)
5. Work experience: 11 years
 - 5.1. Teaching: 11 years
 - 5.2. Research: no
 - 5.3. Industry: no
 - 5.4. Others: no
6. Area of Specialization: VLSI
7. Courses taught at: Under Graduate & Post Graduate
 - 7.1. Diploma:
 - 7.2. Post Diploma:
 - 7.3. Under Graduate: yes
 - 7.4. Post Graduate: yes
 - 7.5. Post Graduate Diploma:
8. Research guidance: No
 - 8.1. No. of papers published in:
 - a) National (Journals / Conferences): 10
 - b) International (Journals / Conferences): 10
 - 8.2. Master:05
 - 8.3. Ph. D:
9. Projects carried out: 78
10. Patents: No
11. Technology Transfer: No
12. Research Publications: No
13. No. of books published with details: No
14. No. of Workshops conducted / attended: 10/25

